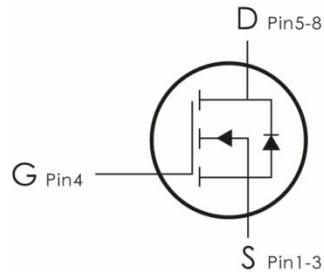
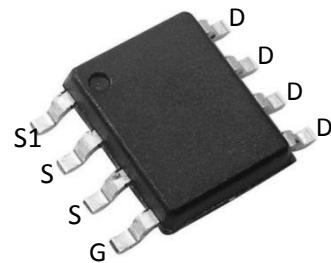


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.



Features:

- 1) $V_{DS}=30V, I_D=10A, R_{DS(ON)}<13m\Omega @V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.

Absolute Maximum Ratings: ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ C$)	10	A
	Drain Current – Continuous ($T_c=100^\circ C$)	6	
I_{DM}	Drain Current – Pulsed	50	
P_D	Power Dissipation ($T_c=25^\circ C$)	2.5	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Thermal Characteristics:

Symbol	Parameter	Max	Units
R_{Theta}	Thermal Resistance,Junction to Ambient ²	50	°C/W

Electrical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	30	33	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
On Characteristics³						
$V_{\text{GS(th)}}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	1	1.6	3	V
$R_{\text{DS(ON)}}$	Static Drain-Source On Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	---	10	13	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$	---	13	18	
G_{FS}	Forward Transconductance	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=10\text{A}$	15	---	---	S
Dynamic Characteristics⁴						
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	1550	---	pF
C_{oss}	Output Capacitance		---	300	---	
C_{rss}	Reverse Transfer Capacitance		---	180	---	
Switching Characteristics⁴						
$t_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DS}}=25\text{V}, I_{\text{D}}=1\text{A}, R_{\text{GEN}}=6 \Omega, V_{\text{GS}}=10\text{V}$	---	30	---	ns
t_r	Rise Time ^{2,3}		---	200	---	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	100	---	ns
t_f	Fall Time ^{2,3}		---	80	---	ns
Q_g	Total Gate Charge ^{2,3}	$V_{\text{GS}}=5\text{V}, V_{\text{DS}}=15\text{V}, I_{\text{D}}=10\text{A}$	---	13	---	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	5.5	---	nC
Q_{gd}	Gate-Drain "Miller" Charge ^{2,3}		---	3.5	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ³	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=10\text{A}, T_J=25^\circ\text{C}$	---	---	1.2	V
I_s	Continuous Source Current ²		---	---	10	A

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Characteristics: ($T_C=25^\circ\text{C}$ unless otherwise noted)

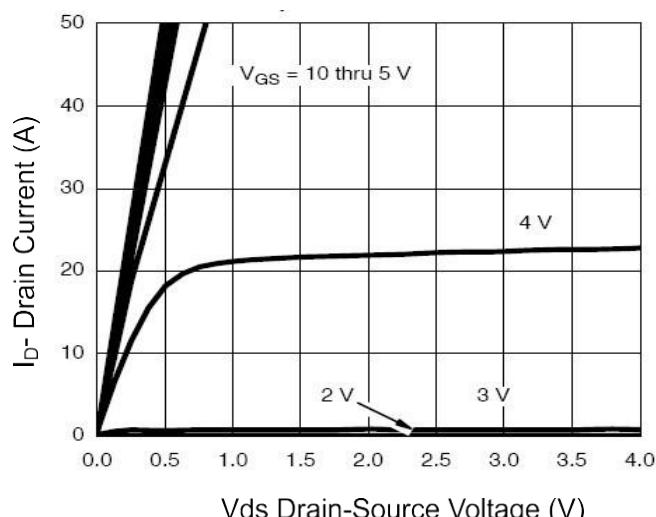


Figure 1 Output Characteristics

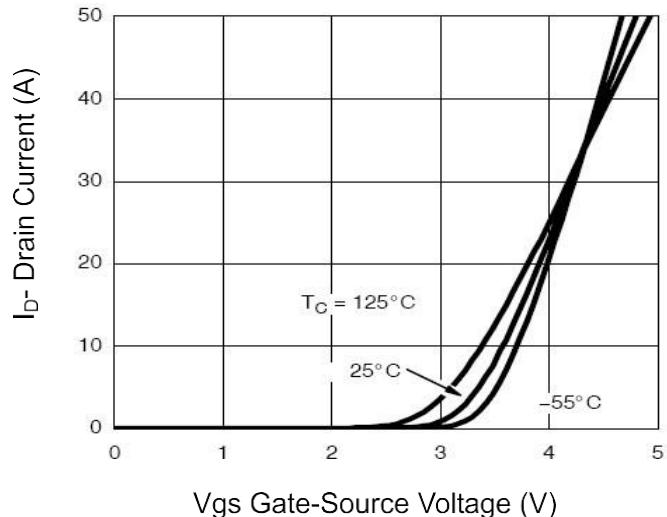


Figure 2 Transfer Characteristics

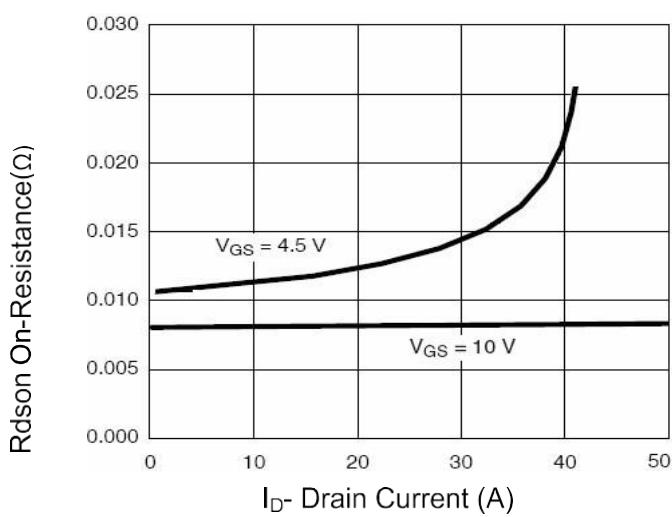


Figure 3 Rdson-Drain Current

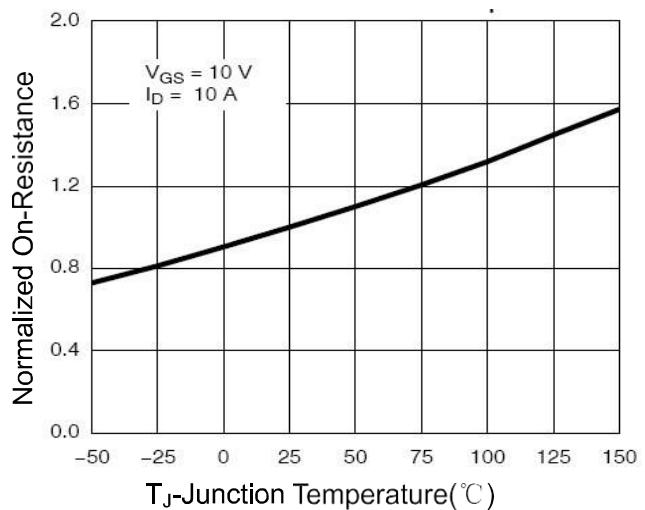


Figure 4 Rdson-JunctionTemperature

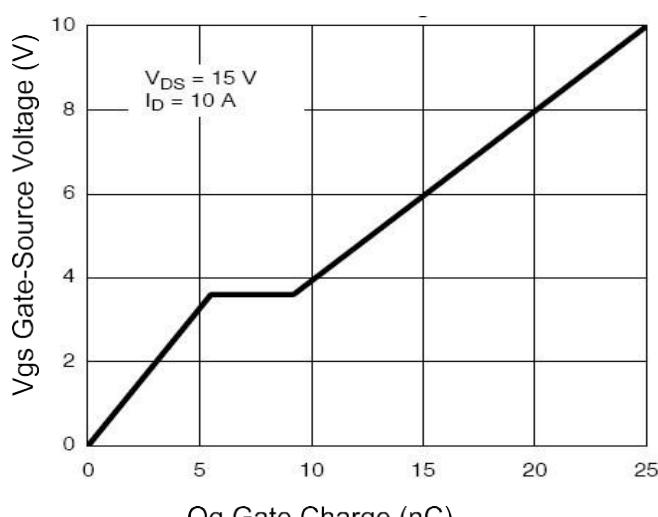


Figure 5 Gate Charge

Figure 6 Source- Drain Diode Forward

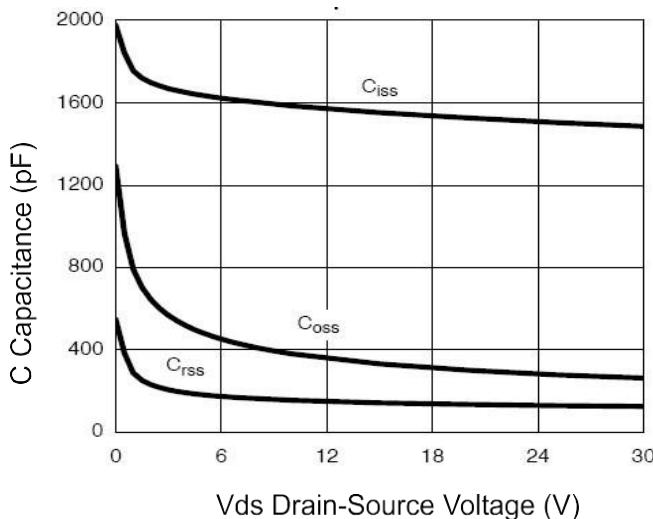


Figure 6 Capacitance vs Vds

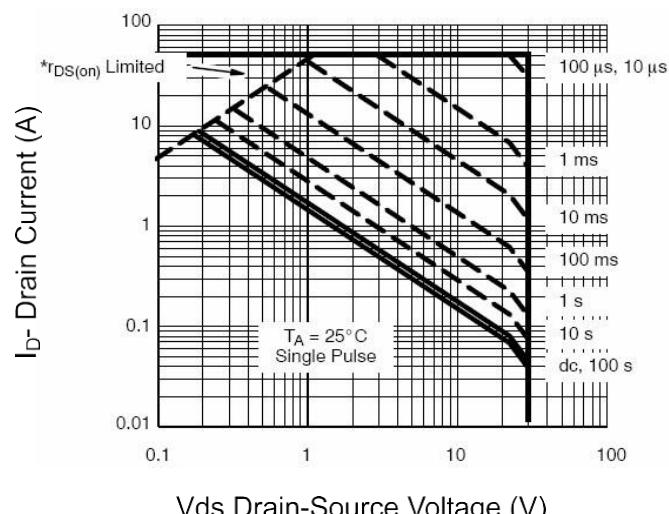


Figure 7 Safe Operation Area

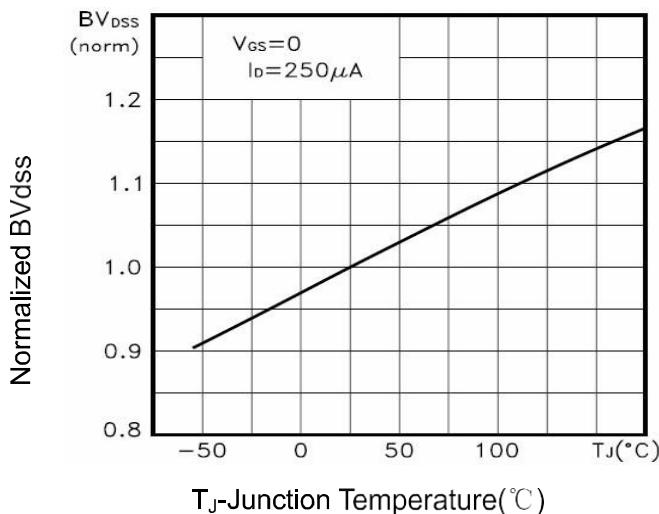


Figure 8 BV_{DSS} vs Junction Temperature

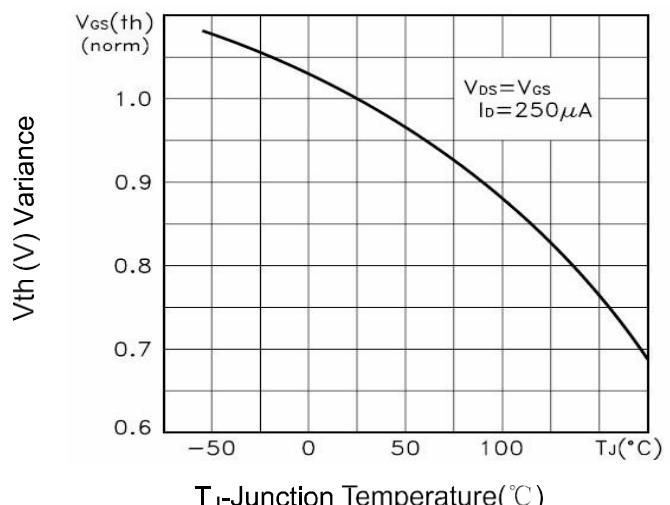


Figure 9 $V_{GS(th)}$ vs JunctionTemperature

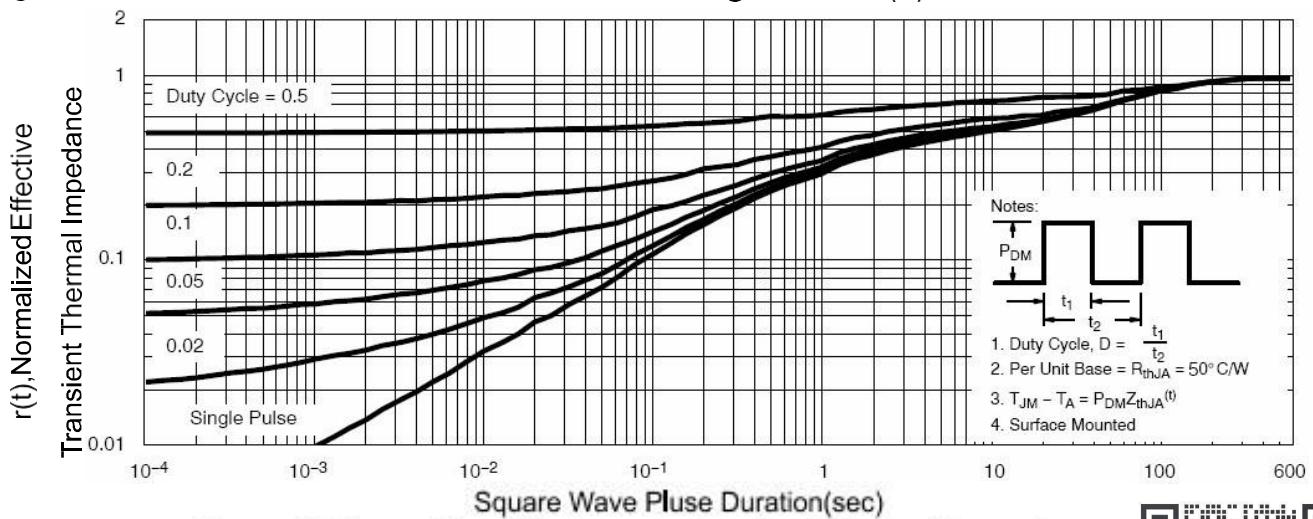


Figure 10 Normalized Maximum Transient Thermal Impedance

